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PTO/SB/21 (09-04)

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Total Number of Pages in This Submission

Application Number	09/705,487
Filing Date	November 3, 2000
First Named Inventor	Jean-Didier Allegrucci
Art Unit	2112
Examiner Name	Paul R. Myers
Attorney Docket Number	X-1768 US

ENCLOSURES (Check all that apply)

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| <input type="checkbox"/> Fee Transmittal Form | <input checked="" type="checkbox"/> Drawing(s) | <input type="checkbox"/> After Allowance Communication to TC |
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Remarks
Replacement Sheet for Figures 1-2

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	XILINX, INC. CUSTOMER NO: 24309		
Signature			
Printed name	Kim Kanzaki		
Date	November 23, 2004	Reg. No.	37,652

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X-1768 US
09/705,487

PATENT
Conf. No.: 7643

IN THE UNITED STATES PATENT OFFICE

Applicant: Jean-Didier Allegrucci
Assignee: Xilinx, Inc.
Title: Diagnostic Scheme for Programmable Logic in a Configurable System on a Chip

Serial No.:	09/705,487	File Date:	November 3, 2000
Examiner:	Paul R. Myers	Art Unit:	2112
Docket No.:	X-1768 US	Conf. No.:	7643

Mail Stop AMENDMENT
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AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the First Office Action after the RCE mailed from the Patent Office on August 26, 2004, please amend the application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Amendments to the Drawings begin on page 6 of this paper and include an amended drawing figure (Fig. 1) and a complete set of formal drawings attached following page 11.

Remarks begin on page 7 of this paper.

An **Appendix** including an amended drawing figure (Figure 1) is attached following page 9.